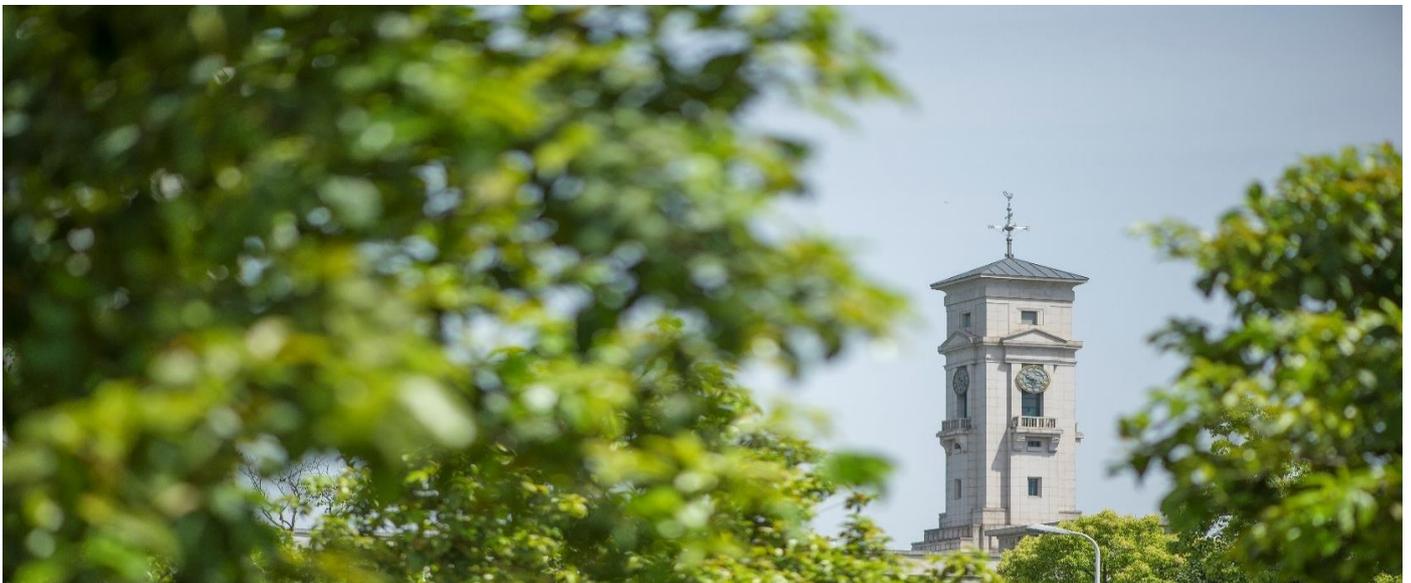


Sampling-time harmonic control for cascaded h-bridge converters with thermal control

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Sampling-Time Harmonic Control for Cascaded H-Bridge Converters with Thermal Control

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Index Terms—Harmonic analysis, Pulse width modulation, Multilevel converters.

Abstract—Cascaded H-bridge converter (CHB) is a multilevel topology that is a well-suited solution for multiple applications such as flexible ac transmission systems or motor drives. This paper is focused on a CHB where the cells present an aging mismatch. This can be caused by the maintenance operation which forces to replace some damaged cells of the converter with new or repaired ones. In this paper, a new improved approach of the active thermal control (ATC) of the CHB using Discontinuous PWM (D-PWM) is presented. The D-PWM technique is used to reduce the power losses of one cell reducing its average temperature in order to increase its remaining lifetime. However, the combination of D-PWM with traditional Phase-Shifted PWM (PS-PWM) introduces high harmonic distortion in the output voltage of the CHB converter at twice the carrier frequency. A detailed harmonic distortion analysis of the CHB output voltage when the D-PWM based ATC is active is presented. From this analysis, a modification of the traditional PS-PWM is derived to eliminate the harmonic distortion at twice the carrier frequency. Experimental results show how the ATC using D-PWM is achieved whereas the harmonic distortion around twice the carrier frequency is eliminated.

I. INTRODUCTION

Nowadays, multilevel converter is a mature technology which has been developed since decades [1]. Multilevel converters are used in a extended range of power applications

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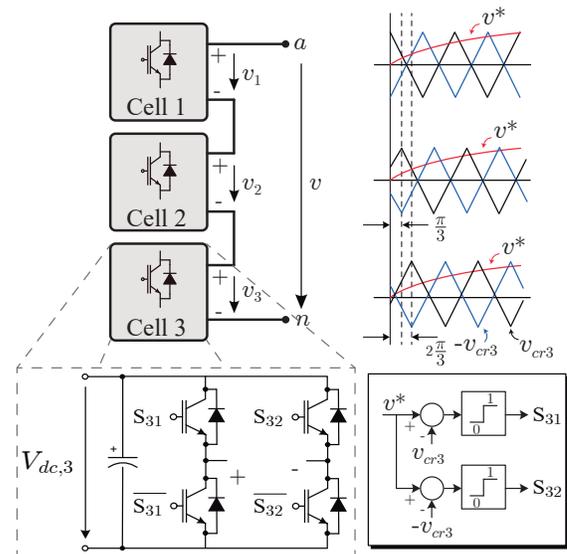


Fig. 1: Three-cell multilevel cascaded H-bridge converter (CHB): Topology and conventional PS-PWM modulation technique

like pumps, fans, power quality applications and also in renewable energy integration [2], [3]. Among this family, a popular multilevel topology is the cascaded H-bridge converter (CHB) which is shown in Fig. 1 and particularized for a three-cell CHB. This topology was proposed for first time in the early 70's by McMurray [4]. CHB is composed by the series connection of several H-bridges as shown in Fig. 1. Traditionally, the basic power cell is the H-bridge, however it is possible to find in the literature other power cells like NPC or T-type [5]. Among other advantages, this topology is very popular because is able to achieve very high nominal output voltages with a large number of voltage levels providing a high modularity and natural fault-tolerant capability. This fact makes the CHB converter a good candidate to build the Smart Transformer (ST) [6], [7]. The high quality of the output voltage and current makes the CHB one of the most used topologies for medium and high-voltage applications [8].

There are many modulation techniques to operate a CHB converter in the literature, from optimized pulses like selective harmonic mitigation [9] to modulation methods based on multi-carriers [10]. In fact, CHB has most frequently been

operated using the well-known phase-shifted PWM (PS-PWM) technique. Each power cell is operated using a conventional unipolar PWM technique applying triangular carriers with frequency f_c . In order to implement the PS-PWM method, it is necessary to apply a phase displacement between triangular carriers of consecutive power cells. This phase displacement is usually defined as an angle which is equal to 180° (π radians) divided by the number of cells available in each phase, denoted by M [11]. In order to illustrate this idea, the PS-PWM concept as well as the unipolar modulation technique are represented in Fig. 1.

As it is well-known, PS-PWM method presents some advantages when it is applied to a CHB converter. As an example, the output voltage presents high equivalent switching frequency because of the PS-PWM multiplicative effect. Therefore, assuming a switching frequency of each power cell equal to f_c , the CHB output voltage features a harmonic behavior that is equivalent to that produced by a converter with a switching frequency equal to $2Mf_c$. In addition, it also provides a power equalization between modules [12] because the voltage references are equal for all cells. Therefore, a natural equal distribution of power losses, temperature and aging is achieved. PS-PWM operation for a CHB is shown in Fig. 2 for two complete periods until $t = 40ms$ with 150 volts as dc capacitor voltages and a modulation index equal to 0.8.

Power converter failure is mainly caused by the cumulative aging of power devices which is proportional to the thermal stress they suffer during the converter operation. Thermal stress leads to a fatigue of the materials of which the semi-conductors are composed. In other words, the reiteration of thermal cycles leads to the expansion and compression of materials which increases the thermal resistance becoming in a non-stop cycle [13]–[15]. This point is well-known in the academia and industry which are focusing their efforts to develop predictive models to obtain the expected remaining power devices lifetime as a function of the suffered thermal cycles [16], [17].

Fault-tolerant capability for CHB allows its operation even if a power cell fault occurs, that means, the maintenance operation is straight-forward because the cells responsible of the fault can be bypassed. In this way, the power converter is able to continue its operation whereas the damaged cells are repaired or substituted by new ones [18]. Consequently, the maintenance operations lead to an aging mismatch among the power cells. That means, the substituted power cells present a different cumulated aging compared with the others, and therefore, after a long-term operation, a non-negligible damage mismatch can be found among the cells [19].

II. OVERVIEW OF ACTIVE THERMAL CONTROL FOR CHB CONVERTERS

Active Thermal Control (ATC) is a control strategy which allows the modification of the thermal stress suffered by the modules taking an action over the electrical parameters [15]. Power devices lifetime model estimates the remaining lifetime as a function of cumulated damage which depends

on the thermal cycles suffered. The power device damage is estimated taking into account the number of thermal cycles and their deepness. The damage is strongly influenced by the actual power devices temperature, causing more damage with increasing temperature [20]. In this way, the ATC modifies the operation of each module managing the power losses and therefore, making possible to decrease the power devices average temperature reducing the impact of future thermal cycles [21].

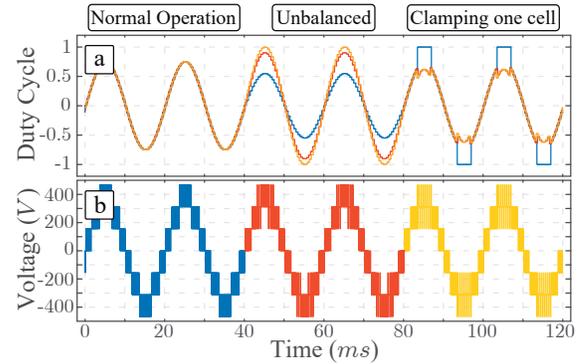


Fig. 2: Active thermal control strategies. a) Duty Cycle b) Output voltage

To develop an effective ATC method for the CHB, two complementary methods can be considered. One possible technique to manage the aging mismatch between modules is to apply a different modulation index to each power cell. In this way, the more damaged cell would produce less voltage than others at its output terminals. As consequence, as the same current flows between all the power modules, each cell manages a different power level. The aged cells will operate with a reduced modulation index whereas the less aged cells compensate this operation mismatch increasing their modulation indexes [22]. This technique is shown in Fig. 2a from 40 to 80ms where the first cell is controlled to have a 30% of reduction in its processed power.

As an alternative, it is possible to deal with the switching losses generated in each power cell using the discontinuous PWM (D-PWM) technique. In other words, it is possible to force the generation of the maximum voltage available in the cell during a part of grid-period, avoiding hence the associated switching losses [23]. Therefore, to maintain the desired operation point, the remaining cells must compensate the voltage excess decreasing their reference voltages. This approach has been also represented in Fig. 2a from 80 to 120ms using a clamping angle equal to 60° .

Both methods (modifying the modulation index [22] and clamping one cell [23]) can be applied in order to design an ATC for the CHB. This means that both methods can manage the temperature of the CHB power devices according to their remaining lifetime. In addition, it can be noticed that both methods are complementary and can be applied simultaneously if it is required. This paper is mainly focused on the study of the ATC method based on clamping one cell in the CHB.

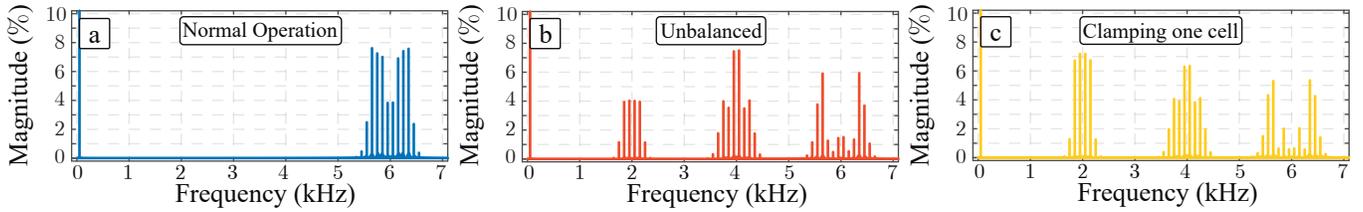


Fig. 3: Output voltage spectrum. a) PS-PWM b) Unbalanced operation c) D-PWM

However, it is important to notice that, when the ATC takes control over the electrical magnitudes, the CHB output voltage is deteriorated increasing its total harmonic distortion (THD) value. It is easy to see that the ATC application leads to an unequal power distribution between the cells and therefore, the natural advantages of PS-PWM technique are partially lost. This phenomenon can be observed taking into account the harmonic spectrum of the CHB output voltage. When the ATC is active forcing an unbalanced operation (different modulation indexes or applying D-PWM), a high harmonic distortion appears located at $2f_c$. This fact is possible to be observed in Fig. 3b and Fig. 3c drawn in red and yellow, respectively considering f_c equal to 1 kHz [24]. The main impact of this low-order harmonic distortion is related to the output filter of the CHB, which has to be designed to keep the output waveforms quality above the minimum standards. If a harmonic distortion at $2f_c$ appears, the required output filter is more expensive, heavy and bulky. In this way, operating properly the CHB this harmonic distortion can be reduced and the output filter is improved in terms of economical cost, weight and size.

This undesirable phenomenon has been studied by the academia in past years leading to the development of some recent modulation techniques. Some of them are based on the feed-forward idea according with real dc voltages present in the converter to carry out some calculations in order to determine the needed duty cycles. As an example, this strategy has been developed in [25], [26] for PS-PWM and [27]–[29] for space-vector modulation techniques. However, none of these proposals deal with the harmonic distortion generated at $2f_c$.

Focusing on harmonic distortion located at twice the carrier frequency, different solutions have been proposed in the literature. For instance, in [30] the conventional PS-PWM technique is modified to reduce the low harmonic content when dc voltages are unequal. This method has been further investigated in [31], [32]. Finally, using the Fourier analysis, in [33] a technique considering the harmonic detail of the pulses generated by PS-PWM is developed. In this way, the method introduced in [33] allows the harmonic cancellation considering different dc voltages and modulation indexes. This is done performing, every sampling time, the analysis of all the H-bridge pulses.

In this paper, a new approach of the ATC using D-PWM for one H-bridge of the CHB is presented. The proposed method is based on the calculation of the displacements angles between carriers in the PS-PWM method each sampling time. The

proposed method is able to eliminate the harmonic distortion located at $2f_c$ under an unbalanced operation of CHB.

It is important to highlight that the ATC based on clamped cells in the CHB is not a contribution of this paper. As commented previously, this idea was presented in [23] clearly demonstrating the good performance of the method from the temperature management point of view. Taking this fact into account, the proposed paper uses the ATC method presented in [23] but modifying its final implementation by changing the modulation strategy of the cells. As the obtained CHB output voltages and currents using the proposed method are very similar (differences are negligible) to those obtained in [23], we can extract the same conclusions using the proposed method than those presented in [23] about the thermal behavior of the clamping cells method. The main contribution of the proposed method compared with [23] is the improvement in the harmonic response because the harmonic distortion at low frequency is eliminated. Furthermore, the proposed method can be compared with that proposed in [34] where the phase displacement angles in the PS-PWM method are obtained by complex calculations every fundamental period. In [34], it can be observed that the ATC is achieved successfully using the clamping-cell method as expected. The idea introduced in [34] helps to eliminate the distortion specifically located at twice the carrier frequency but it ignores the sidebands of that group of harmonics. In the proposed method, as the phase displacement angles are determined every sampling period, that harmonics group is completely eliminated improving [34] results.

III. SAMPLING-TIME HARMONIC ANALYSIS OF THE CHB OUTPUT VOLTAGE USING PS-PWM

In the CHB operation, the voltage generated by each power cell can be interpreted and modeled as a square pulse train which depends on a variable duty cycle D . This concept is clearly shown in Fig. 4 where the output voltage generated by a single power cell is plotted (in blue) using an conventional unipolar modulation technique. It is also possible to observe the voltage reference drawn in red. A detail of this waveform is presented in Fig. 4 highlighting a single period T_c .

The output voltage waveform of the k -th power cell of the CHB depends on a variable duty cycle which is defined as the ratio between the output voltage reference proposed by the control scheme (v_k^*) and the dc voltage ($V_{dc,k}$):

$$D_k = \frac{v_k^*}{V_{dc,k}}, \text{ where } D_k \in [-1, 1] \quad (1)$$

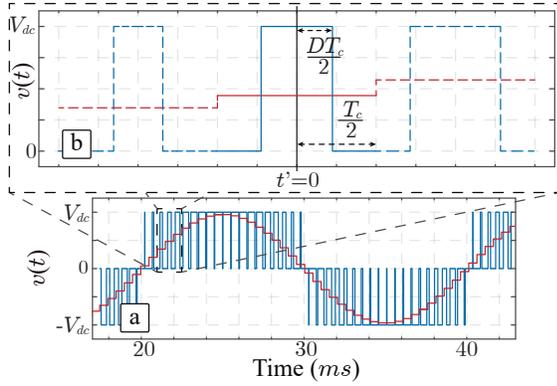


Fig. 4: Output voltage of an H-bridge applying a unipolar PWM method. a) A complete period b) A detail

In order to study the spectral components of the k -th H-Bridge output voltage $v_k(t)$, it is analyzed using the Fourier expansion series. This mathematical tool asserts that, under mild assumptions, a periodic signal can be expressed as a dc component plus an infinite sum of sine and cosine products as follows:

$$v_k(t) = \frac{a_{0k}}{2} + \sum_{n=1}^{\infty} \left[a_{nk} \cos(n\omega t) + b_{nk} \sin(n\omega t) \right] \quad (2)$$

where $\omega = 2\pi/(T_c/2)$ because $2f_c$ is considered the fundamental frequency in the analysis.

Focusing on Fig. 4b and choosing the time origin smartly on t' , $v_k(t)$ presents an even symmetry. This is significantly advantageous because the coefficients calculation is simplified due to b_{nk} term is zero. Therefore, it is only required to calculate the a_{nk} coefficients as follows:

$$\begin{aligned} a_{nk} &= \frac{1}{T_c} \int_{-T_c/2}^{T_c/2} v_k(t) \cos(n\omega t) dt \\ &= \frac{2}{T_c} \int_0^{D T_c/2} V_{dc,k} \cos(n\omega t) dt \\ &= \frac{2V_{dc,k}}{n\pi} \left[\sin\left(\frac{2\pi n}{T_c} t\right) \right] \Bigg|_0^{D T_c/2} \\ &= \frac{2V_{dc,k}}{n\pi} \sin(n\pi D_k) \end{aligned} \quad (3)$$

In Fig. 5, it is represented a comparison between the output voltage of an H-bridge and its Fourier expansion series using the previous calculations (up to 20-th harmonic content). The Fourier expansion series matches with the pulse train being the only difference between both signals the peaks due to the Gibbs effect [35].

So, using the Fourier coefficients, the output voltage of k -th H-bridge can be expressed as:

$$v_k(t) = V_{dc,k} D_k + \sum_{n=1}^{\infty} \left[\frac{2V_{dc,k}}{n\pi} \sin(n\pi D_k) \cos(n\omega t) \right] \quad (4)$$

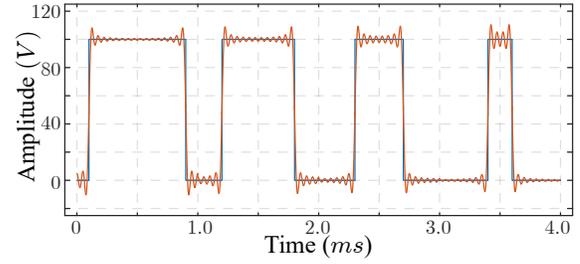


Fig. 5: Output voltage of an H-bridge and the waveform obtained with its Fourier expansion series expression considering harmonics up to $n = 20$ -th

All power modules have the same voltage expression and the difference between their output voltages in a CHB is related to the phase displacement present in the PS-PWM method. Conventionally, for a CHB with M -cells per phase operating with PS-PWM and using an unipolar PWM per cell, the phase displacement of k -th power cell is defined by:

$$\phi_k = (k-1) \frac{\pi}{M} \quad \text{with } k = 1, \dots, M \quad (5)$$

Considering the angle displacement given by (5), the k -th power cell output voltage can be re-written as:

$$v_k(t) = V_{dc,k} D_k + \sum_{n=1}^{\infty} \left[a_{nk} \cos(n\omega t - n\phi_k) \right] \quad (6)$$

According to the linearity property of the Fourier expansion series, the total CHB output voltage can be directly calculated as the sum of the output voltages of the M power cells, i.e.:

$$\begin{aligned} v(t) &= \sum_{k=1}^M v_k(t) \\ &= \sum_{k=1}^M \left[V_{dc,k} D_k + \sum_{n=1}^{\infty} \left[a_{nk} \cos(n\omega t - n\phi_k) \right] \right] \end{aligned} \quad (7)$$

From (7), the n -th order harmonic content of the total CHB output voltage can be described by:

$$v]_n(t) = \sum_{k=1}^M a_{nk} \cos(n\omega t - n\phi_k), \quad (8)$$

Finally, applying the well-known cosine sum trigonometric formula $\cos(a \pm b) = \cos(a) \cos(b) \mp \sin(a) \sin(b)$ to decouple the Fourier harmonic coefficients from the carrier terms, (8) can be rewritten as:

$$\begin{aligned} v]_n(t) &= \cos(n\omega t) \sum_{k=1}^M \left[a_{nk} \cos(n\phi_k) \right] \\ &\quad + \sin(n\omega t) \sum_{k=1}^M \left[a_{nk} \sin(n\phi_k) \right]. \end{aligned} \quad (9)$$

IV. VARIABLE ANGLE PS-PWM TECHNIQUE

In order to eliminate the n -th order harmonic distortion of the CHB output voltage, it is necessary to make zero the expressions inside square brackets in (9). If first harmonic order is considered ($n = 1$ which means $2f_c$ component) in a CHB with three cells ($M = 3$), it is necessary to solve the following system of equations in order to eliminate it:

$$\begin{aligned} a_{11} + a_{12} \cos(\phi_2) + a_{13} \cos(\phi_3) &= 0 \\ a_{12} \sin(\phi_2) + a_{13} \sin(\phi_3) &= 0 \end{aligned} \quad (10)$$

where ϕ_1 was considered 0° for convenience.

The solution to these equations is given by

$$\begin{aligned} \phi_2 &= \arccos\left(\frac{-a_{11}^2 - a_{12}^2 + a_{13}^2}{2a_{11}a_{12}}\right) \\ \phi_3 &= \arccos\left(\frac{-a_{11}^2 + a_{12}^2 - a_{13}^2}{2a_{11}a_{13}}\right) \end{aligned} \quad (11)$$

If the CHB is working under balanced conditions (same dc voltage for all the power cells $V_{dc,k} = V_{dc}$ and same duty cycle $D_k = D$), all Fourier coefficients a_{nk} are equal ($a_{nk} = a_n$). Applying these conditions in (11), the calculation of the phase displacement angles is simplified leading to

$$\begin{aligned} \phi_2 &= \arccos\left(-\frac{1}{2}\right) \rightarrow \phi_2 = 120^\circ, 240^\circ \\ \phi_3 &= \arccos\left(-\frac{1}{2}\right) \rightarrow \phi_3 = 240^\circ, 120^\circ \end{aligned} \quad (12)$$

These values of ϕ_k are those used in the conventional PS-PWM method. If these angles are considered, substituting them in (9) for the second harmonic, it can be demonstrated that $v_{|2}=0$. This result was expected because in the definition of the conventional PS-PWM method it is said that all harmonic distortion is zero up to $2Mf_c$ component (which coincides with $v_{|3}$).

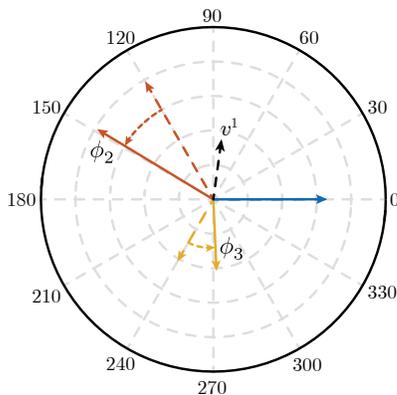


Fig. 6: Representation of the output voltages of the CHB cells and the corresponding harmonic content of the CHB output voltage at $2f_c$. Traditional angles in dashed lines and modified angles in solid lines.

However when the CHB is working under unbalanced conditions, each coefficient a_{nk} is different. In this case, if the

conventional values of ϕ_k calculated in (12) are applied, $v_{|1}$ and $v_{|2}$ are not zero in general. So, the idea of the variable-angle PS-PWM method is to calculate the phase displacement angles needed to be applied depending on the instantaneous operational power converter conditions. Each sampling period, the a_{1k} coefficients have to be determined using (3), and finally the angles to be applied are calculated using (11).

TABLE I: Example of unbalanced operation point for a three-cell CHB.

| H-Bridge parameter | 1 | 2 | 3 |
|--------------------|------|------|------|
| $V_{dc,k}$ [V] | 90 | 80 | 85 |
| m_k | 0.75 | 0.60 | 0.85 |

As an example, in Table I is presented a possible unbalanced operation point for a three-cell CHB power converter. Assuming this scenario, the vector system that describes this situation (during one sampling time) has been drawn in Fig. 6. As observed, the resulting fundamental harmonic $v_{|1}$ is different to zero when the conventional PS-PWM method is applied. However, using (11) it is possible to calculate new phase displacements angles to be applied in the PS-PWM method in order to cancel $v_{|1}$. The new angle disposition that removes this harmonic component is drawn using solid vectors in Fig. 6.

On the other hand, it can be affirmed that the valid solutions for the displacement angles ϕ_2 and ϕ_3 can be obtained only if the following conditions are met:

$$\begin{aligned} |a_{11} - a_{12}| &\leq a_{13} \leq (a_{11} + a_{12}) \\ |a_{11} - a_{13}| &\leq a_{12} \leq (a_{11} + a_{13}) \end{aligned} \quad (13)$$

In addition, according to (11), if some a_{1k} coefficient is equal to zero, the proposed variable angle technique does not present solution because of division by zero is not defined. In this way, the three-cell PS-PWM angle set must be fixed following the rule:

$$\begin{aligned} \text{if } a_{11} = 0 &\rightarrow [\phi_1, \phi_2, \phi_3] = [0, 0, 180]^\circ \\ \text{if } a_{12} = 0 &\rightarrow [\phi_1, \phi_2, \phi_3] = [0, 0, 180]^\circ \\ \text{if } a_{13} = 0 &\rightarrow [\phi_1, \phi_2, \phi_3] = [0, 180, 0]^\circ \end{aligned} \quad (14)$$

During the CHB operation, each sampling time the outer control loop provides the reference voltage v^* to be generated by the CHB in order to achieve some specific control target. This reference voltage is conventionally shared equally by all power cells, achieving an inherent power equalization between the cells. So, the k -th reference voltage v_k^* is determined by

$$v_k^* = \frac{v^*}{M} \quad (15)$$

However, as was commented in section II, this conventional operation of the CHB can be changed in order to manage the power cell aging by modifying slightly the modulation index of each cell. This can be done including the weighting factor λ_k leading to

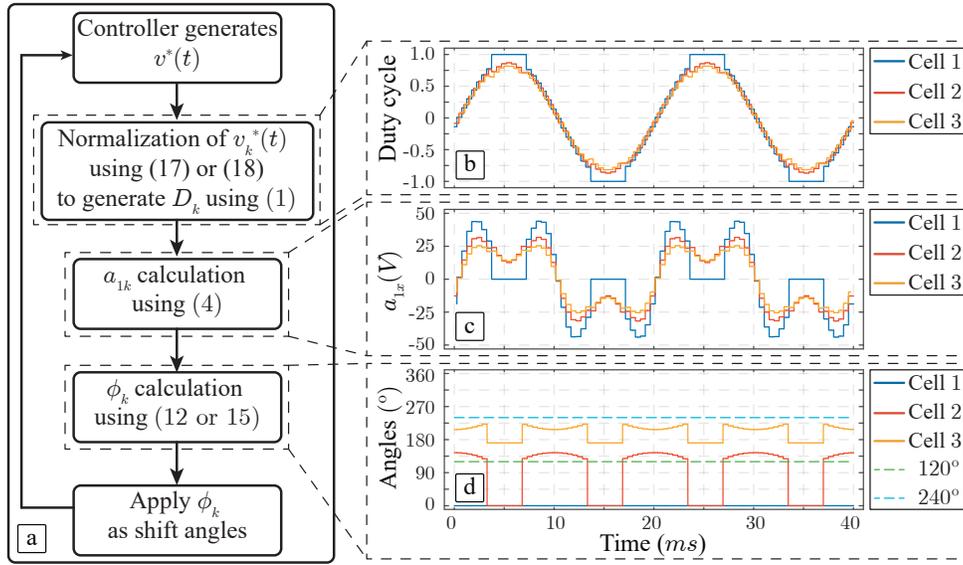


Fig. 7: Variable-angle flowchart application example for two complete grid periods. a) Linear flowchart. b) Duty cycle generation c) First harmonic content. d) Variable angle disposition.

$$v_k^* = \lambda_k \frac{v^*}{M} \quad \text{where} \quad \sum_{k=1}^M \lambda_k = M \quad (16)$$

Each cell reference voltage v_k^* is normalized respect to its corresponding dc voltage using (1), determining the duty cycle D_k represented in Fig. 7b. In addition, the n -th harmonic content of v_k^* is determined by (3). Applying this, the first harmonic content of k -th power cell (a_{1k}) is also drawn in Fig. 7c. Finally, according to the instantaneous operational conditions of the CHB and using (11), the angles to be applied in each power cell are obtained (see Fig. 7d where the conventional PS-PWM angles are also drawn in dashed lines).

As shown in Fig. 7b, a clamping period for the first power cell equal to 60° is applied. It has to be noticed that during the time where this cell is clamped, this specific cell reference voltage is saturated to the maximum value (positive or negative) and therefore the remaining power cells are forced to reduce their instantaneous duty cycles accordingly to achieve the same total reference voltage v^* . So, during this time, expression (15) is not fulfilled and the cell reference voltages are determined using

$$\begin{aligned} v_1^* &= \pm V_{dc,1} \\ v_k^* &= \frac{v^* \mp v_1^*}{M-1} \lambda_k \quad \text{with } k = 2, \dots, M \end{aligned} \quad (17)$$

This effect can be observed in Fig. 7b twice per cycle. Applying a duty cycle D_1 equal to ± 1 in (3), the resulting harmonic content a_{11} is equal to zero as it is shown in Fig. 7c. The angles calculation using (11) cannot be used in this case since the resulting angles would be undetermined. As a consequence, the angles are calculated using (14).

As a summary, in Fig. 7a the algorithm to implement the proposed variable-angle modulation technique in a control

platform is presented. This flowchart has been implemented by simulation using a three-cell CHB power converter working under the unbalanced operation point presented in Table I. This routine must be run every sampling period because the phase displacement angles ϕ_k depend on the instantaneous values of duty cycles D_k and the dc voltages $V_{dc,k}$.

It is important to notice that the presented method is focused on a three-cell CHB but it can be extended for a CHB with a larger number of cells. However, the complexity of the corresponding equations to be solved, similar to those introduced in (10), increases as the number of cells grows. An initial approach to face this problem has been presented in [36].

V. EXPERIMENTAL RESULTS

In order to validate the good performance, the proposed strategy has been tested in the laboratory prototype shown in Fig. 8. Each H-bridge is composed by four IXYB82N120C3H1 IGBTs from IXYS and the three-cell CHB converter is operated by the MPC5643L dual core 32-bit microprocessor from Freescale Semiconductor [37], [38]. The case temperature of one IGBT in each cell (S_{x1} in Fig. 1) is measured using fiber optics sensors (OTG-F) with a signal conditioner ProSens [39]. The temperature achieved by each IGBT inside cell will be approximately the same because the cell is modulated by unipolar PWM. The most important parameters and passive elements used in the experiment setup are summarized in Table II. To fix the voltage of the power cells, independent dc power supplies are used.

Three different scenarios (denoted as I, II and III) have been also tested and summarized in Table III. All experiments consider the CHB operating an ATC method with one clamped cell applying D-PWM (clamping angle equal to 60°) following the next structure: in the first 100 ms the behavior of the CHB using the traditional PS-PWM modulation technique is shown,

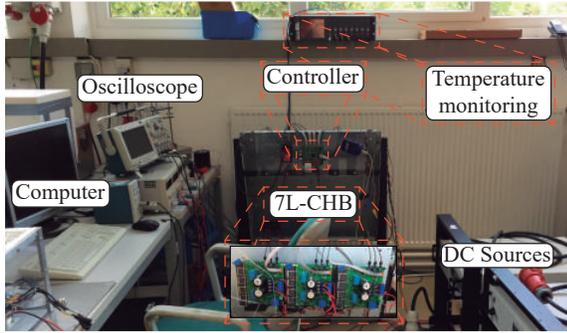


Fig. 8: Experimental setup composed by a three-cell CHB power converter, temperature datalogger and dc sources.

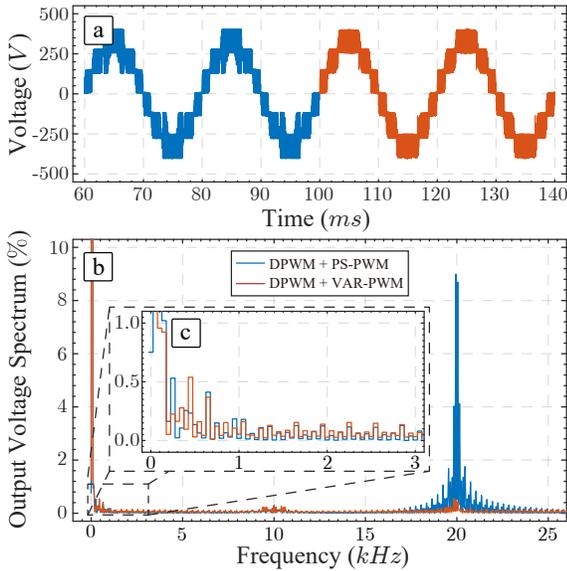


Fig. 9: Experimental result for operation point proposed by experiment I in Table III. PS-PWM is drawn in blue and variable-angle is plotted in red. a) CHB output voltage b) Harmonic spectrum c) Detail of the spectrum

using the blue color. After that, the proposed variable-angle modulation takes the control of the CHB operation and the results are drawn using red color.

Experiment I studies the behavior of the CHB converter when the operation point has different dc voltages and equal modulation indexes. Associated experimental results are shown in Fig. 9.

Experiment II proposes equal dc voltages but different

TABLE II: CHB parameters setup, passive elements and unbalanced operation point considered for the experiment.

| Parameter | Value |
|--------------------------------------|-------|
| Number of cells in the CHB | 3 |
| Cell switching frequency f_c (kHz) | 10 |
| Cell capacitance (mF) | 2.2 |
| Clamping angle ϕ_c ($^\circ$) | 60 |
| Load Inductance (mH) | 0.3 |
| Load Resistance (Ω) | 10 |

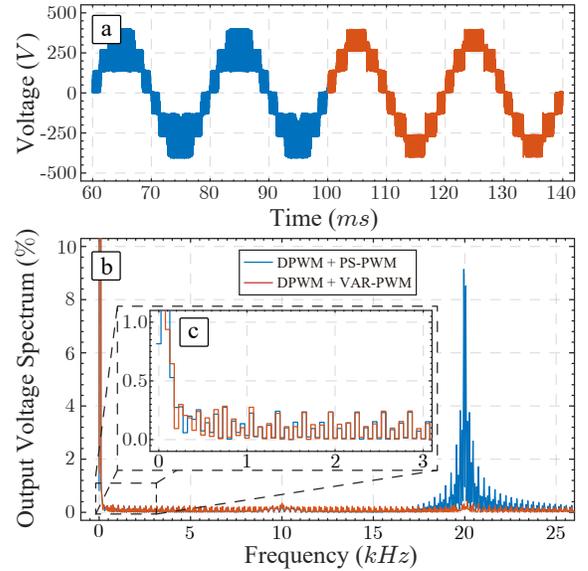


Fig. 10: Experimental result for operation point proposed by experiment II in Table III. PS-PWM is drawn in blue and variable-angle is plotted in red. a) CHB output voltage b) Harmonic spectrum c) Detail of the spectrum

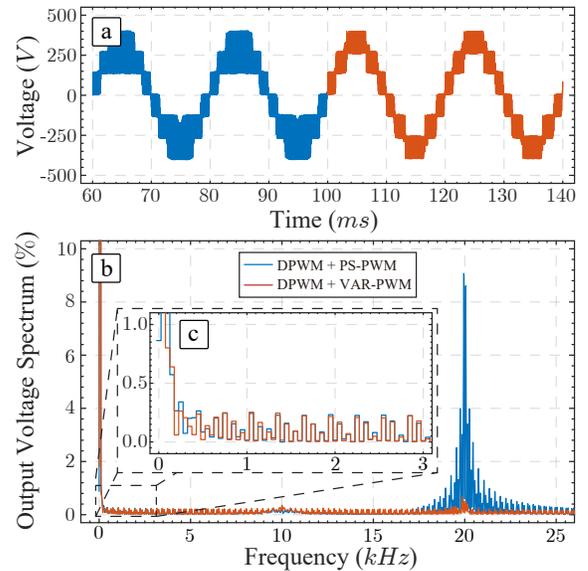


Fig. 11: Experimental result for operation point proposed by experiment III in Table III. PS-PWM is drawn in blue and variable-angle is plotted in red. a) CHB output voltage b) Harmonic spectrum c) Detail of the spectrum

modulation indexes. The harmonic spectrum of the output voltage is presented in Fig. 10.

Finally, experiment III is devoted to study the behavior of the CHB converter in a totally unbalanced operation, that is, different dc voltages and modulation indexes. The output voltage harmonic spectrum can be consulted in Fig. 11.

All experiments have in common the high output voltage distortion located at $2f_c$ when the PS-PWM modulation is used in combination with D-PWM technique. It is clear that the harmonic distortion as well as the typical base-bands present at

TABLE III: Proposed experimental setup scenarios

| Experiment | DC Voltage [V] | Duty Cycles |
|------------|-----------------|-----------------|
| I | [125, 135, 145] | [0.8, 0.8, 0.8] |
| II | [135, 135, 135] | [0.5, 0.9, 1.0] |
| III | [134, 130, 140] | [0.5, 0.9, 1.0] |

$2f_c$ are completely removed (or massively reduced, depending on the case) when the variable-angle technique is applied. Moreover, a zoomed version of the low-order components of the output voltage spectrum is also provided. As it is highlighted in Fig. 9c, Fig. 10c and Fig. 11c, the variable-angle technique does not affect negatively to the low-order components. This fact can be consulted in Table IV where the THD as well as the weighted THD (WTHD) values have been experimentally measured up to 50-th harmonic order.

TABLE IV: THD and WTHD considering up to 50-th harmonic order extracted from experimental results

| Experiment | PS-PWM | | Variable-angle PS-PWM | |
|------------|---------|----------|-----------------------|---------|
| | THD [%] | WTHD [%] | THD[%] | WTHD[%] |
| I | 1.826 | 0.701 | 1.629 | 0.578 |
| II | 1.670 | 0.663 | 1.482 | 0.523 |
| III | 1.637 | 0.632 | 1.360 | 0.462 |

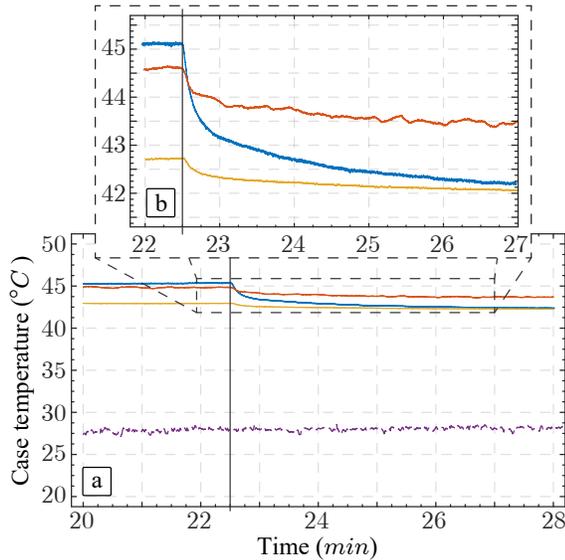


Fig. 12: Thermal experimental results for experiment III in Table III. IGBT case temperature in each power cell. The color scheme is as follows: first cell is drawn in blue, red is used for second cell and third cell is plotted in yellow. Ambient temperature is purple

Finally, it is necessary to validate the CHB thermal behavior. In this way, in Fig. 12 the device case temperature of each cell is shown when the proposed strategy is used. The experiment is carried out under a constant ambient temperature equal to 28°C . Until $t = 22.5$ minutes, the CHB is conventionally operated using PS-PWM considering the operation point proposed in experiment III. In this scenario, the power cells reach case temperatures equal to 45.1 , 44.6 and 42.7°C , respectively.

It has to be noticed that all power devices in one cell achieve the same case temperature due to the symmetric operation of the unipolar PWM method.

After $t = 22.5$ minutes of operation, the D-PWM based ATC method (clamping angle equal to 60°) with variable-angle PS-PWM is activated leading to a significant temperature reduction as observed in Fig. 12. It demonstrates the effectiveness of the D-PWM with variable-angle PS-PWM method to achieve the active thermal control. Taking the experimental results into account, it can be affirmed that the reliability function introduced in [40] will be improved because using the clamped-cell based ATC method the obtained power losses are reduced. Finally, it can be noticed that from a superior level, an operation & maintenance system will manage the power converter determining every sampling period the value of the clamping angle in order to optimize the operation & maintenance system costs. At first sight, the balance of the aging mismatch could be a proper target but other options could be also attractive taking into account other issues as pre-programmed system maintenance or potential fault tolerant capability features.

VI. CONCLUSIONS

Reducing as much as possible the maintenance costs is a key factor of commercial power converters. Prognostic maintenance should be implemented in order to improve the power converter availability. In a modular structure such as the CHB, the possibility to delay the failure of one cell by means of active thermal control could allow to implement planned maintenance. Unfortunately, the active thermal control methods applied to CHB usually introduce harmonic distortion at low frequency which is a drawback in terms of output waveforms degraded quality and corresponding extra filtering costs.

In this paper, the active thermal control method based on modifying the modulation indexes of the power cells including a discontinuous PWM technique for one cell is used. In order to avoid the disadvantages of this active thermal control method, a modification of the conventional PS-PWM technique is applied. The proposed method is a variable angle PS-PWM where the phase displacement angles between consecutive power cells are not fixed. The calculation of the phase displacement angles to be applied is based on the Fourier analysis of the power cell waveforms. The required calculations are computationally simple and are carried out every sampling time.

Experimental results are included in the paper in order to validate the proposed method. It is demonstrated that the thermal control of the CHB topology is correctly applied while the low-order harmonic distortion located at twice the carrier frequency is eliminated. The experimental results show how the proposed strategy improves the harmonic spectrum of the output waveforms without affecting the thermal control.

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